



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

See application of: **Dimpsey et al.**

Serial No.: **10/806,917**

Filed: **March 22, 2004**

For: **Method and Apparatus for
Autonomic Test Case Feedback Using
Hardware Assistance for Data
Coverage**

35525

PATENT TRADEMARK OFFICE
CUSTOMER NUMBER

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Group Art Unit: **2122**

Examiner: **Unknown**

Attorney Docket No.: **AUS920040063US1**

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- Form PTO-1449;
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Respectfully submitted,

[Signature]
Duke W. Yee

Registration No. 34,285

YEE & ASSOCIATES, P.C.

P.O. Box 802333

Dallas, Texas 75380

(972) 385-8777

ATTORNEY FOR APPLICANTS



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Dimpsey et al.

Serial No: **10/806,917**

Filed: **March 22, 2004**

Title: **Method and Apparatus for Autonomic
Test Case Feedback Using Hardware
Assistance for Data Coverage**

Group Art Unit: **2122**

Intellectual Property
Law Department
International Business
Machines Corporation
11400 Burnet Road
Austin, Texas 78758

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
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Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

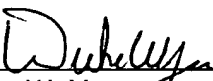
While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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No item of information cited in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this Supplemental Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

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Respectfully submitted,


Duke W. Yee
Registration No. 34,285
Yee & Associates, P.C.
P.O. Box 802333
Dallas, Texas 75380
Telephone: (972) 385-8777



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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number	10/806,917
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Unknown
Art Unit	2122
Attorney Docket No.	AUS920040063US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5,797,019	08-18-1998	Levine et al.	
AB	6,549,998 B1	04-15-2003	Pekarich et al.	
AC	2003/0101367 A1	05-29-2003	Bartfai et al.	
AD	5,113,507	05-12-1992	Jaeckel	
AE	6,681,387 B1	01-20-2004	Hwu et al.	
AF	6,925,424 B2	08-02-2005	Jones et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

Examiner:

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.